

Logic for Computer Science



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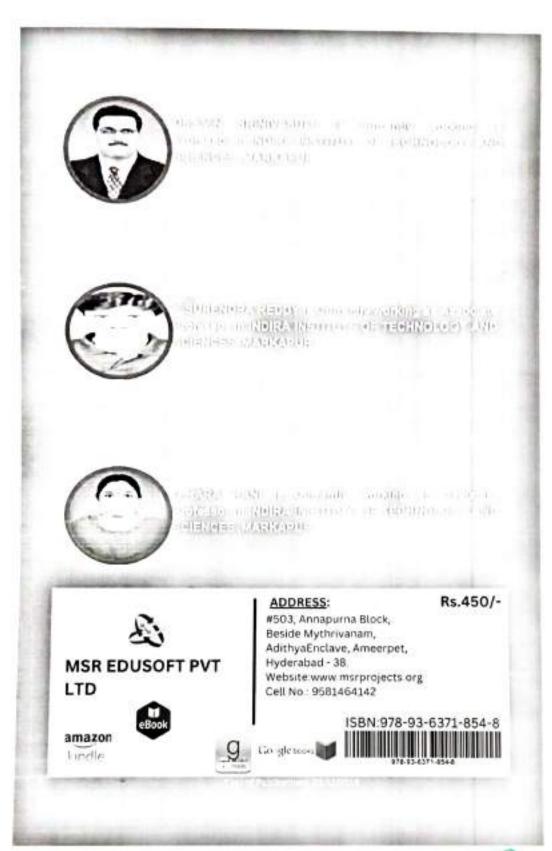


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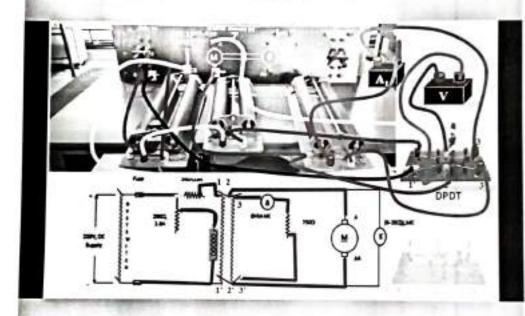
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INTRODUCTION TO MATLAB FOR ENGINEERING STUDENTS



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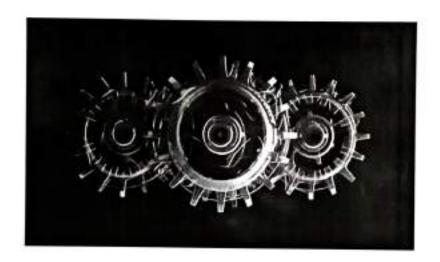
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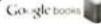
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Number System and Boolean Algebra And Switching Functions:

Number Systems. Base Conversion Methods, Complements of Numbers, Codes: Binary Codes. Binary Codes Binary Coded Decimal Code and its Properties, Unit Distance Codes, Error Detecting and Correcting Codes, Hamming Code. Boolean Algebra:

Basic Theorems and Properties, Switching Functions, Canonical and Standard Forms, Algebraic Simplification of Digital Logic Gates, Properties of XOR Gates, Universal Logic Gates Multilevel NAND/NOR realizations.

UNIT -II: 41 89

Minimization and Design of Combinational Circuits:

K. Map Method, up to Five variable K. Maps, Don't Care Map Entries, Prime and Essential prime implications, Quine Mc Cluskey Tabular Method, Combinational Design, Arithmetic Circuits, Comparator, decoder, Encoder, Multiplexers, DeMultiplexers, Code Converters.

UNIT -III: 90-104

Sequential Machines Fundamentals:

Introduction, Basic Architectural Distinctions between Combinational and Sequential circuits, classification of sequential circuits, The binary cell, The S-R-Latch Flip-Flop The D-Latch Flip-Flop. The "Clocked T" Flip-Flop, The "Clocked J-K" Flip-Flop, Design of a Clocked Flip-Flop, Conversion from one type of Flip-Flop to another, Timing and Triggering Consideration.

UNIT -IV: 105-133

Sequential Circuit Design and Analysis:

introduction, State Diagram, Analysis of Synchronous Sequential Circuits, Approaches to the Design of Synchronous Sequential Finite State Machines, Design Aspects, State Reduction, Design Steps, Realization using Flip-Flops Counters - Design of Ripple Counter, Synchronous counter, Ring Counter, Registers, Shift Register.

UNIT -V: 134-156

Sequential Circuits:

Finite state machine- capabilities and limitations ,Mealy and Moore models, a minimization of completely specified and incompletely specified sequential machines, Partition techniques and Merger chart methods-concept of minimal cover table.

Algorithmic State Machines:

Salient features of the ASM chart-Simple examples- Weighing machine and Binary multiplier.

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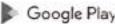
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Applied Strength of Materials for Engineering Technology

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KANDULA RAJA SEKHARA REDDY
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GROOVI TECHNO IT SOLUTION PRIVATE LIMITED

A TEXT BOOK OF



Applied Strength of Materials for Engineering Technology



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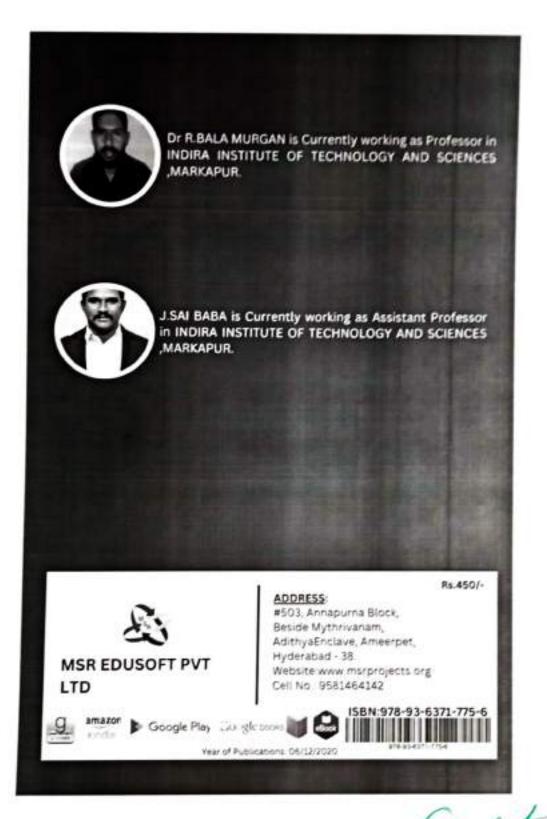
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CONCRETE TECHNOLOGY

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A TEXT BOOK OF



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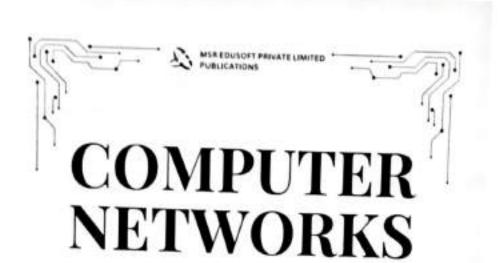
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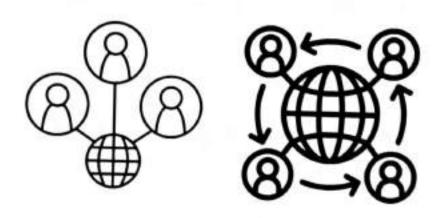
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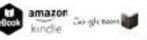
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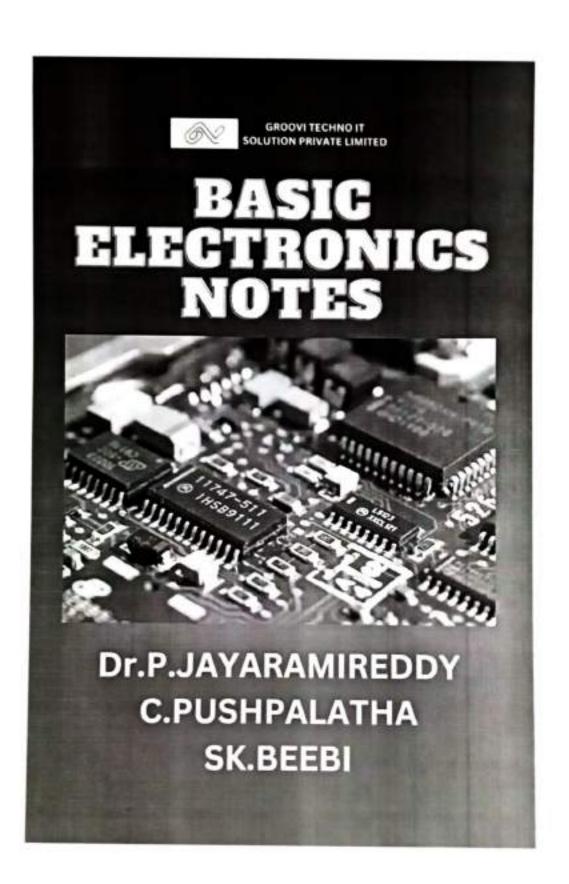
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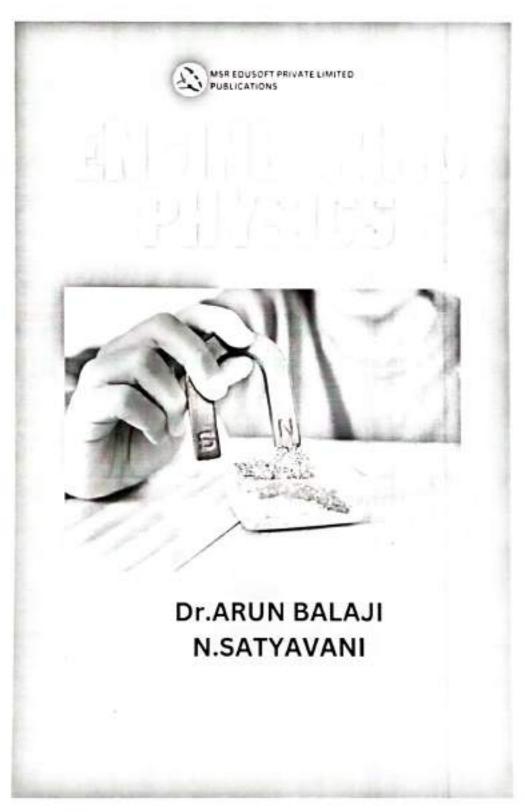
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